

**REMARKS**

Claims 1, 4-12, 14 and 15 are pending in this application.

By this Amendment, claim 1 is amended for better clarity and to incorporate the subject matter recited in claim 2. Claim 12 is similarly amended. Accordingly, claim 2 is canceled.

Claims 4-8 and 15 are amended for better clarity and to be consistent with the amendments made to claims 1 and 12. Claims 3 and 13 are canceled.

Reconsideration of the application in light of the foregoing claim amendments and the following remarks is respectfully requested.

Applicant thanks Examiner Piziali for the courtesy extended to Applicant's representative, Mr. Luo, during the September 23, 2005 personal interview. The substance of the personal interview is incorporated in the following remarks.

The Office Action rejects claims 2, 3, 6, 13 and 15 under 35 U.S.C. §112, second paragraph. The subject matter recited in claim 2 is incorporated in claim 1, and claim 2 is canceled. Claims 3 and 13 are also canceled. Claims 6 and 15 are amended for better clarity. Accordingly, withdrawal of the rejection of claims 2, 3, 6, 13 and 15 under 35 U.S.C. §112, second paragraph is respectfully requested.

The Office Action rejects claims 1-10 and 12-15 under 35 U.S.C. §102(e) over U.S. Patent No. 6,876,348 to Murata et al. ("Murata"); and rejects claim 11 under 35 U.S.C. §103(a) over Murata. These rejections are respectfully traversed.

Claim 1, as amended, recites a second switch that selects a zero-data signal when pixel data is not stored in a storage unit. Similarly, claim 12, as amended, recites selecting a zero-data signal when pixel data is not stored in the storage unit. As is known in the art, such a feature requires no reversal of the polarity of a counter electrode, at least when the zero-data

signal indicates that no pixel data is stored in the storage unit. Murata does not disclose or suggest such a feature, or the advantage thereof.

As discussed during the interview, Murata discloses a pixel 10 that includes a pixel portion 100 and a SRAM portion 200. See Figs. 3 and 4, and col. 3, lines 23-47. During a normal driving mode, the pixel portion 100 receives an analog video signal from the signal line 11 when the switch SW-P is turned on, and applies the analog signal to a pixel electrode 12. See Figs. 4 and 5, and col. 4, line 64 - col. 5, line 4. During a SRAM driving mode, an inverter 15 of the SRAM portion 200 converts 9V analog signals into 10V binary signals, and 5.5V analog signals into 5V binary signals. An inverter 16 converts the binary signals from the inverter 15. The binary signals from the inverters 15 and 16 are alternatively applied to the pixel electrode 12 through operation of switches SW-A and SW-B. See Figs. 4 and 5, and col. 5, lines 59-67. Murata requires a reversal of the polarity of a counter electrode voltage. See col. 5, lines 4-12, and col. 6, lines 26-29. Murata merely discloses selecting signals between inverters 15 and 16. Murata does not disclose or suggest selecting a zero-data signal when pixel data is not stored in the storage unit.

The Office Action asserts that Murata discloses normal/writing mode in Fig. 5, and thus discloses selecting a zero-data signal when pixel data is not stored in the storage unit. However, as discussed during the interview, the normal/writing mode in Fig. 5 of Murata uses an analog potential, not a zero-data signal. The Office Action appears to assert that the analog potential in Fig. 5 of Murata corresponds to a zero-data signal. However, as discussed above, this analog potential is the video signal supplied to the signal line 11 shown in Fig. 4 of Murata, and is supplied to the pixel electrode 12. The analog potential cannot be zero because, if the analog potential were zero, there would be no potential difference between the pixel electrode and the counter electrode 13. Such an arrangement would defeat the purpose

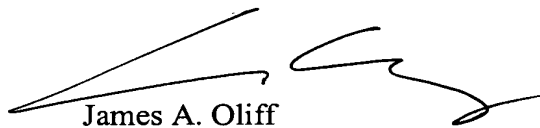
of Murata because, in such an arrangement, the video signals would not be able to be displayed.

Thus, Murata does not disclose or suggest the subject matter recited in claims 1 and 12, and claims 4-11, 14 and 15 depending therefrom. Accordingly, withdrawal of the rejection of claims 1, 4-12, 14 and 15 under 35 U.S.C. §102(e) or §103(a) is respectfully requested.

In view of the foregoing, it is respectfully submitted that this application is in condition for allowance. Favorable reconsideration and prompt allowance of claims are earnestly solicited.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,

  
James A. Oliff  
Registration No. 27,075

Gang Luo  
Registration No. 50,559

JAO:GXL/sqb

Date: September 28, 2005

**OLIFF & BERRIDGE, PLC**  
**P.O. Box 19928**  
**Alexandria, Virginia 22320**  
**Telephone: (703) 836-6400**

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